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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Kazuto IKEDA et al.**

Group Art Unit: **1753**

Serial Number: **10/685,796**

Examiner: **Steven H. Versteeg**

Filed: **October 16, 2003**

Confirmation No.: **7017**

For: **METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE
HAVING SILICIDE LAYER**

Attorney Docket Number: **032016**

Customer Number: **38834**

AMENDMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

August 29, 2005

Sir:

This paper is filed in response to the Office Action dated June 29, 2005.

Amendments to the Specification begin on page 2 of this paper.

Remarks begin on page 2 of this paper.